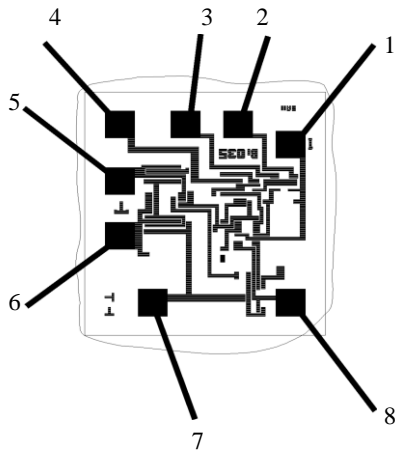
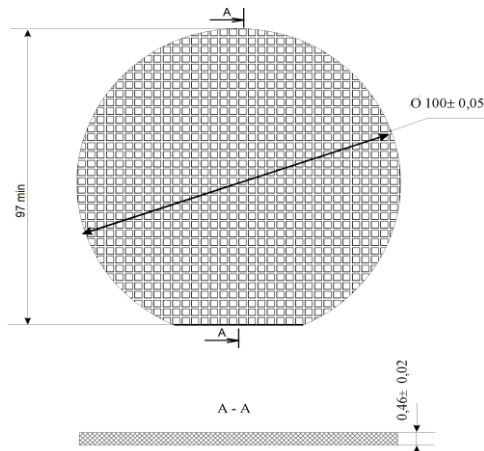


Operational Amplifiers α RD709AH4

Size of chip: (1,1x1,2)mm

Pin	Designation
1	Frequency Compensation
2	Inverting input
3	Noninverting input
4	Negative Supply
5	Frequency Compensation
6	Output
7	Positive Supply
8	Frequency Compensation

Electrical Characteristics

 $T_A = +25^\circ\text{C}$

Parameter	Conditions	Min Value	Max value	Units
Input Offset Voltage	$V_{S1} = 16.5 \text{ V}, V_{S2} = -16.5 \text{ V}, R_L = 10 \text{ k}\Omega$	-	5	mV
Input Offset Current	$V_{S1} = 16.5 \text{ V}, V_{S2} = -16.5 \text{ V}, R_L = 10 \text{ k}\Omega$	-	0,3	mkA
Input Bias Current	$V_{S1} = 16.5 \text{ V}, V_{S2} = -16.5 \text{ V}, R_L = 10 \text{ k}\Omega$	-	0,7	mkA
Common Mode Rejection Ratio	$V_{S1} = 15 \text{ V}, V_{S2} = -15 \text{ V}, R_L = 10 \text{ k}\Omega, U_{IC}=8\text{V}$	70	-	dB
Supply Current	$V_{S1} = 16.5 \text{ V}, V_{S2} = -16.5 \text{ V}, R_L = 10 \text{ k}\Omega, U_I=0,15\text{V}$	-	4,5	mA
Output Voltage Swing	$V_{S1} = 15 \text{ V}, V_{S2} = -15 \text{ V}, R_L = 2 \text{ k}\Omega,$	$\pm 10,5$		V
Large Signal Voltage Gain	$V_{S1} = 15 \text{ V}, V_{S2} = -15 \text{ V}, R_L = 2 \text{ k}\Omega,$	20	80	V/mV
Output voltage	$V_{S1} = 15 \text{ V}, V_{S2} = -15 \text{ V}, R_L = 2 \text{ k}\Omega,$ $f=300\text{-}400 \text{ Hz}, U_I=0,05\text{V}$	100	150	mV
Slew Rate	$V_{S1} = 15 \text{ V}, V_{S2} = -15 \text{ V}, R_L = 2 \text{ k}\Omega,$ $f=300\text{-}400 \text{ Hz}$	0,06	-	V/ μ s
Setting time	$V_{S1} = 15 \text{ V}, V_{S2} = -15 \text{ V}, R_L = 2 \text{ k}\Omega,$ $f=1000 \text{ Hz}$	-	2,5	mks
Input resistance	$V_{S1} = 15 \text{ V}, V_{S2} = -15 \text{ V}, R_L = 10 \text{ k}\Omega$ $f=50 \text{ Hz}$	100	-	k Ω

Microcircuits are made under supervision of Quality Department, checked and there correspond specification