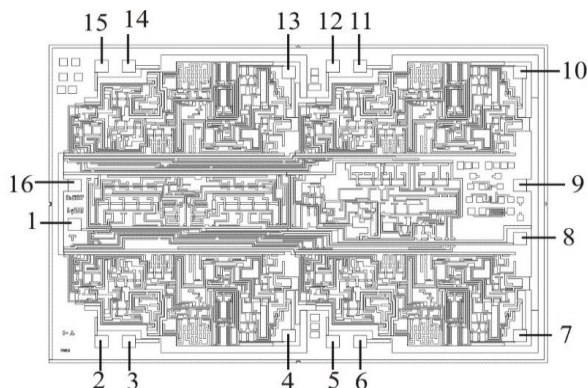
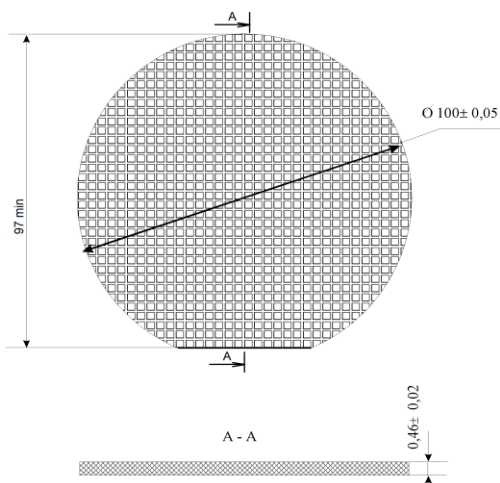


Gated Voltage Comparator  $\alpha$ RD707H4


Size of chip: (4,9x3,4)mm

Pin Connection Diagram

Pin	Pin Destination	Pin	Pin Destination
1	Logic input	9	Power $U_{CC2}$ ( minus )
2	Input 1	10	Output 3
3	Storage capacity 1	11	Storage capacity 3
4	Output 1	12	Input 3
5	Input 2	13	Output 4
6	Storage capacity 2	14	Storage capacity 4
7	Output 2	15	Input 4
8	Ground	16	Power $U_{CC1}$ ( plus )

## Electrical Characteristics

 $T_A = +25^\circ\text{C}$ 

Parameter	Max	Min	Units
Zero offset voltage in sampling mode for each channel, $U_{IOV}$ , mV, $U_{CC} = \pm 12,7 \text{ V}$ ; $Sc = 330 \text{ pF}$ ; $Rc = 100 \text{ }\Omega$ ; $U_I = 0 \text{ V}$ ; $R_L = 10 \text{ k}\Omega$ ; $U_I \text{ contr} = +3 \text{ V} \dots +5 \text{ V}$ ;	-15	15	mV
Zero offset voltage at the transition to storage mode for each channel, $ U_{IOh} $ $U_{CC} = \pm 12,7 \text{ V}$ ; $Sc = 390 \text{ pF}$ ; $Rc = 100 \text{ }\Omega$ ; $U_I = 0$ ; $R_L = 10 \text{ k}\Omega$ ; $U_I \text{ contr} = 5 \text{ V}$ ; $U_I \text{ contr} = 0 \text{ V}$ ; $F \text{ contr} = 5 \dots 30 \text{ kHz}$	-	10	mV
Sample mode gain for each channel $U_{CC} = 12,7 \text{ V}$ ; $U_O = 6 \text{ V}$ ; $R_L = 5,5 \text{ k}\Omega$	0,95	1,05	
Supply current, $I_{CC}$ $U_{CC} = \pm 12,7 \text{ V}$ ; $U_I = 0$ ; $U_I \text{ contr} = 3 \text{ V}$ ; $Sc = 390 \text{ pF}$ ; $Rc = 100 \text{ }\Omega$	-	21	mA
Supply current, $I_{CC}$ $U_{CC} = \pm 12,7 \text{ V}$ ; $U_I = 0 \text{ V}$ ; $U_I \text{ contr} = 3 \text{ V}$ ; $Sc = 390 \text{ pF}$ ; $Rc = 100 \text{ }\Omega$	-21	-	mA
Input current in sampling mode for each channel, $I_{IV}$ $U_{CC} = \pm 12,7 \text{ V}$ ; $U_I = 0 \text{ V}$ ; $U_I \text{ contr} = 5 \text{ V}$	-	15	mkA
The rate of change of the output voltage in storage mode for each channel, $V_{UO}$ $U_{CC} = \pm 12,7 \text{ V}$ ; $C_{xp} = 390 \text{ pF}$ ; $R_{xp} = 100 \text{ }\Omega$ ; $U_I = 5 \text{ V}$ ; $R_L = 10 \text{ k}\Omega$ ; $U_I \text{ contr} = 0,8 \text{ V}$ ; $F \text{ contr} = 50 \text{ Hz}$	-8	8	mV/ms
Sample time for each channel, $U_{CC} = \pm 9 \text{ V}$ ; $C_{xp} = 390 \text{ pF}$ ; $R_{xp} = 100 \text{ }\Omega$ ; $U_{IS} = 0$ ; $U_{IE} = 5 \text{ V}$ ; $R_L = 10 \text{ k}\Omega$ ; $C_L = 10 \text{ pF}$ ; $U_I \text{ contr} = +3 \text{ V}$ ; $C_L = 10 \text{ pF}$	-	0,27	mks
Zero offset voltage difference in sampling mode between channels, $\Delta U_{IO} $ , $U_{CC} = \pm 12,7 \text{ V}$ ; $R_L = 10 \text{ k}\Omega$ ; $U_I = 0$ ; $U_I \text{ contr} = +3 \text{ V}$ ;	-	7	mV
Sample mode gain difference between channels $U_{CC} = \pm 9 \text{ V}$ ; $R_L = 10 \text{ k}\Omega$ ; $U_I = 0$ ; $U_I \text{ contr} = +3 \text{ V}$ ; $U_I = -5 \text{ V} \dots 5 \text{ V}$	0,975	1,025	

Microcircuits are made under supervision of Quality Department, checked and there correspond specification