



Pin	Pin Destination	Pin	Pin Destination
1	Logic input 1	9	Power U_{CC2} (minus)
2	Power U_{CC1} (plus)	10	Amplifier output 2
3	General conclusion of the digital circuit	11	Amplifier input 2
4	Amplifier input 3	12	Amplifier output 1
5	Amplifier output 3	13	Amplifier input 1
6	Amplifier input 4	14	Logic input 4
7	Amplifier output 4	15	Logic input 3
8	General conclusion of the analog circuit, Ground	16	Logic input 2

Electrical Characteristics

Table 1

Parameter	T_A	Min	Max	Units
Zero offset voltage, referred to the output, of each channel, $ U_{OO} $ $U_{CC1}= 12,7V$; $U_{CC2}= -12,7 V$; $R_L=5,5 k\Omega$; $K_U= -30$; channel inputs - "break"	+22 \pm 3	-	80	mV
	-45(+ 5-0)	-	100	
	+85(+0-3)	-	100	
Voltage ratio for each channel, K_u $U_{CC1}= 12,7 V$; $U_{CC2}= -12,7 V$; $U_o = 6 V$; $R_L=5,5 k\Omega$ When changing the code combinations at the logical inputs, the transmission coefficients of the channels must change in accordance with Table 2.	+22 \pm 3	-31,5	-28,5	
	-45(+ 5-0)	-10,5	-9,5	
	+85(+0-3)	-4,2	-3,8	
		-2,1	-1,9	
		-1,05	-0,95	
Maximum output voltage of each channel, U_o max, $U_{CC1}= 9 V$; $U_{CC2}= -9 V$; $U_o = 6 V$; $R_L=5,5 k\Omega$	+22 \pm 3	6	-6	V
	-45(+ 5-0)	6	-6	
	+85(+0-3)	6	6	
Supply curent, I_{CC} $U_{CC1}= 12,7 V$; $U_{CC2}= -12,7 V$; $U_I = 0 V$	+22 \pm 3	-	14	mA
	-45(+ 5-0)	-	14	
	+85(+0-3)	-	14	
Supply curent, I_{CC} $U_{CC1}= 12,7 V$; $U_{CC2}= -12,7 V$; $U_I = 0 V$	+22 \pm 3	-14	-	mA
	-45(+ 5-0)	-14	-	
	+85(+0-3)	-14	-	
Low level input current on logic inputs, I_{I1} $U_{CC1}= 12,7 V$; $U_{CC2}= -12,7 V$; $U_{I\ contr} = 0 V$	+22 \pm 3	-	12	mkA
	-45(+ 5-0)	-	12	
	+85(+0-3)	-	12	
High level input current on logic inputs, I_{I2} $U_{CC1}= 12,7 V$; $U_{CC2}= -12,7 V$; $U_{I\ contr} = 0 V$	+22 \pm 3	-	0,1	mkA
	-45(+ 5-0)	-	0,1	
	+85(+0-3)		1	
Slew rate of each gain channel, S_{UOM} $U_{CC1}= 9 V$; $U_{CC2}= -9 V$; $U_{I\ S} = -5,4$; $U_{I\ e} = 5,4V$; $R_L = 5,5 k\Omega$; $C_L = 10 pF$; $K_u = -30$	+22 \pm 3	45	-	V/mks
Output voltage rise time of each channel, t_r $U_{CC1}= 9 V$; $U_{CC2}= -9 V$; $U_{I1} = 0 V$; $U_{I2} = -0,2 V$; $R_L=5,5 k\Omega$; $A_U=-30$; $C_L= 10 pF$; $\varepsilon=2\%$	+22 \pm 3	-	270	ns
Zero bias voltage difference, reduced to the output, between channels, $\Delta U_{OO} $ $U_{CC1}= 12,7V$; $U_{CC2}= -12,7 V$; $R_L=5,5 k\Omega$; $K_U= -30$; channel inputs - "break"	+22 \pm 3	-	30	mV
	-45(+ 5-0)	-	35	
	+85(+0-3)	-	35	

Specification RDM 506D2

Table 1

Parameter	T_A	Max	Min	Units
The relative change in the transmission coefficients for voltage. between channels, ΔKU , $U_{CC1} = 12,7V$; $U_{CC2} = -12,7 V$; $R_L = 5,5 k\Omega$; $K_U = -10$; $U_o = 6 V$	+22 \pm 3	-	2,5	%
	-45(+ 5-0)		3	
	+85(+0-3)		3	

Table 2

Control inputs			Transfer ratio
1	3	4	
1	0	0	minus 30
0	0	0	minus 20
X	0	0	minus 20
X	1	0	minus 10
X	0	1	minus 4
X	0	1	minus 2
X	1	1	minus 1

Microcircuits are made under supervision of Quality Department, checked and there correspond specification