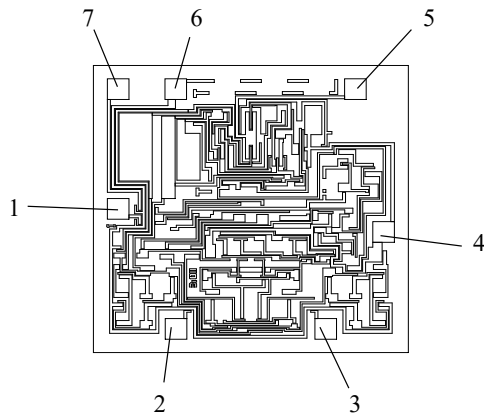
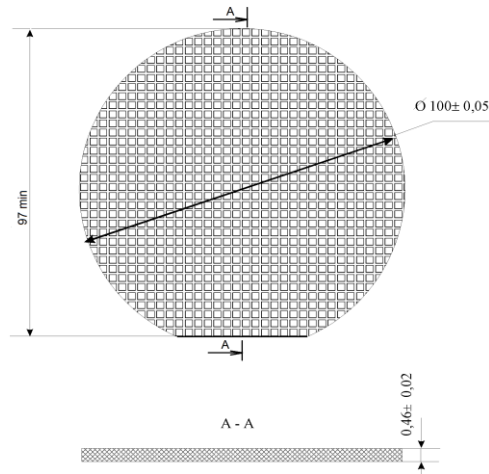


**Operational amplifier αRD2700AH4**


Pin	Connection
1	Balance
2	Inverting input
3	Noninverting input
4	Power Supply $V_{S2}$ ( minus )
5	Output
6	Power Supply $V_{S1}$ ( plus )
7	Balance

Size of chip: (2,0x2,1)mm

 Electrical Characteristics  $T_A = +25^\circ\text{C}$ 

Parameter	Conditions	Min	Max	Units
Input Offset Voltage	$V_{S1} = 13.5\text{ V}, V_{S2} = -13.5\text{ V}, R_G = 50\text{ k}\Omega$	-3	3	mV
Output Voltage Swing	$V_{S1} = 13.5\text{ V}, V_{S2} = -13.5\text{ V}, R_L = 2\text{ kW}$	11	-11	V
Input Bias Current	$V_{S1} = 16.5\text{ V}, V_{S2} = -16.5\text{ V}$	-	20	nA
Input Offset Currents	$V_{S1} = 16.5\text{ V}, V_{S2} = -16.5\text{ V}$	-	10	nA
Positive Supply Current	$V_{S1} = 16.5\text{ V}, V_{S2} = -16.5\text{ V}$	-	120	$\mu\text{A}$
Voltage Gain	$V_{S1} = 13.5\text{ V}, V_{S2} = -13.5\text{ V}, R_L = 5\text{ k}\Omega$	150	-	V/mV
Common Mode Rejection	$V_{S1} = 13.5\text{ V}, V_{S2} = -13.5\text{ V}$	86	-	dB
Slew Rate	$V_{S1} = 13.5\text{ V}, V_{S2} = -13.5\text{ V}$	10		V/ $\mu\text{s}$

Microcircuits are made under supervision of Quality Department, checked and there correspond specification