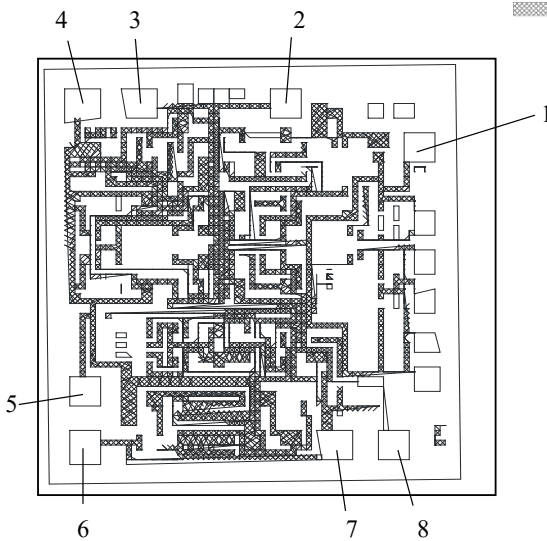
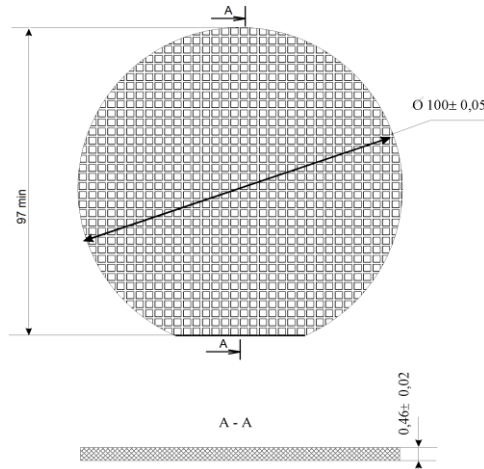


Operational amplifier α RD2520H4



Pin	Connection
1	Balance
2	Inverting input
3	Noninverting input
4	Power Supply V_{S2} (minus)
5	Balance
6	Output
7	Power Supply V_{S1} (plus)
8	Balance

Size of chip: (1,8x1,7)mm

Electrical Characteristics $T_A = +25^\circ\text{C}$

Parameter	Conditions	Min	Max	Units
Input Offset Voltage	$V_{S1} = 16.5 \text{ V}, V_{S2} = -16.5 \text{ V}, R_L \geq 10 \text{ k}\Omega$	-9	9	mV
Output Voltage Swing	$V_{S1} = 13.5 \text{ V}, V_{S2} = -13.5 \text{ V}, R_L = 2 \text{ kW}, U_I = 0,15\text{B}$	9,5	-9,5	V
Input Bias Current	$V_{S1} = 16.5 \text{ V}, V_{S2} = -16.5 \text{ V}, R_L \geq 10 \text{ k}\Omega$	-	225	nA
Input Offset Currents	$V_{S1} = 16.5 \text{ V}, V_{S2} = -16.5 \text{ V}, R_L \geq 10 \text{ k}\Omega$	-	30	nA
Positive Supply Current	$V_{S1} = 16.5 \text{ V}, V_{S2} = -16.5 \text{ V}$	-	7	mA
Voltage Gain	$V_{S1} = 13.5 \text{ V}, V_{S2} = -13.5 \text{ V}, R_L = 2 \text{ kW}, U_o = \pm 10\text{B}$	8	-	V/mV
Common Mode Rejection	$V_{S1} = 13.5 \text{ V}, V_{S2} = -13.5 \text{ V}$	82	-	dB
Slew Rate	$V_{S1} = 13.5 \text{ V}, V_{S2} = -13.5 \text{ V}, R_L \geq 10 \text{ kW}$	80	-	V/ μ s

Microcircuits are made under supervision of Quality Department, checked and there correspond specification



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