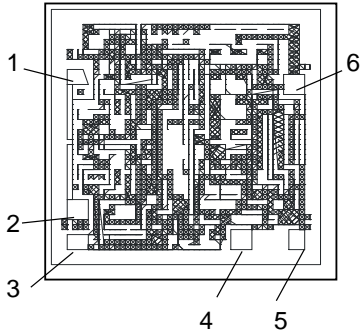
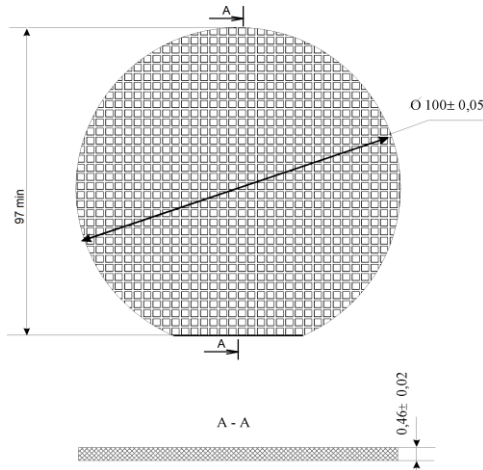


Operational amplifier αRD1544AH4



Pin	Connection
1	Inverting input
2	Noninverting input
3	Power Supply V_{S2} (minus)
4	Balance
5	Output
6	Power Supply V_{S1} (plus)

Size of chip: (1,9x1,9)mm

Electrical Characteristics $T_A = +25^\circ\text{C}$

Parameter	Conditions	Min	Max	Units
Input Offset Voltage	$V_{S1} = 16.5\text{ V}, V_{S2} = -16.5\text{ V}, R_G = 50\ \Omega, R_L \geq 10\text{ k}\Omega$	-6	6	mV
Output Voltage Swing	$V_{S1} = 13.5\text{ V}, V_{S2} = -13.5\text{ V}, R_L = 5\text{ k}\Omega$	10	-10	V
Input Bias Current	$V_{S1} = 16.5\text{ V}, V_{S2} = -16.5\text{ V}, R_G = 50\ \Omega, R_L \geq 10\text{ k}\Omega$	-	1,2	μA
Input Offset Currents	$V_{S1} = 16.5\text{ V}, V_{S2} = -16.5\text{ V}, R_G = 50\ \Omega, R_L \geq 10\text{ k}\Omega$	-	0,3	μA
Positive Supply Current	$V_{S1} = 16.5\text{ V}, V_{S2} = -16.5\text{ V}, R_G = 50\ \Omega, R_L \geq 10\text{ k}\Omega$	-	7	mA
Voltage Gain	$V_{S1} = 13.5\text{ V}, V_{S2} = -13.5\text{ V}, R_L = 5\text{ k}\Omega$	8	-	V/mV
Common Mode Rejection	$V_{S1} = 13.5\text{ V}, V_{S2} = -13.5\text{ V}$	74	-	dB
Slew Rate	$V_{S1} = 13.5\text{ V}, V_{S2} = -13.5\text{ V}$	400		V/ μs

Microcircuits are made under supervision of Quality Department, checked and there correspond specification



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