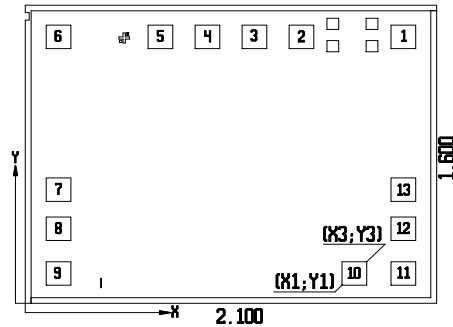
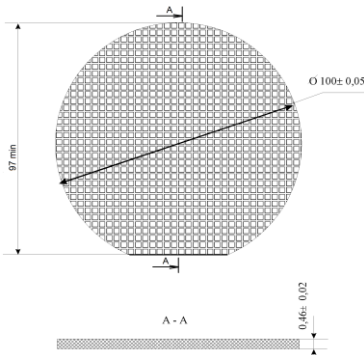




αRD303H4 die on the common non-scribed wafer

Datasheet

Scope: Quad SPST Analog Switch



Pin No	Description	x1	y1	x3	y3	Pin No.	Description	x1	y1	x3	y3
1	Analog Input 1	1,867	1,367	0,993	1,493	8	Logic Input 2	0,107	0,337	0,233	0,463
2	Analog Output 3	1,347	1,367	1,473	1,493	9	Positive Power Supply	0,107	0,097	0,233	0,223
3	Analog Input 3	1,107	1,367	1,233	1,493	10	Ground	1,617	0,097	1,743	0,223
4	Analog Output 4	0,867	1,367	0,993	1,493	11	Negative Power Supply	1,867	0,097	1,993	0,223
5	Analog Input 4	0,627	1,367	0,753	1,493	12	Logic Input 1	1,867	0,337	1,993	0,463
6	Analog Input 2	0,107	1,367	0,233	1,493	13	Analog Output 1	1,867	0,547	1,993	0,673
7	Analog Output 2	0,107	0,547	0,233	0,673						

Electrical characteristics at  $T=(25 \pm 5)^{\circ}\text{C}$

Parameter, units, conditions	Limits	
	min	max
Source Leakage Current, $I_{LS}$ , nA, at $U_S=(-15 \dots 15)\text{V}$ ; $U_{c1}=(13,5 \dots 16,5)\text{V}$ ; $U_{c2}=(-16,5 \dots -13,5)\text{V}$ ; $U^0=(0 \dots 0,8)\text{V}$ ; $U^1=(4 \dots U_{c1})\text{V}$ .	-	70
Drain Leakage Current, $I_{LD}$ , nA, at $U_S=(-15 \dots 15)\text{V}$ ; $U_{c1}=(13,5 \dots 16,5)\text{V}$ ; $U_{c2}=(-16,5 \dots -13,5)\text{V}$ ; $U^0=(0 \dots 0,8)\text{V}$ ; $U^1=(4 \dots U_{c1})\text{V}$ .	-	70
Low Level Input Current, $I_{IL}$ , mA, at $U_{c1}=(13,5 \dots 16,5)\text{V}$ ; $U_{c2}=(-16,5 \dots -13,5)\text{V}$ ; $U^0=(0 \dots 0,8)\text{V}$ .	-	0,2
High Level Input Current, $I_{IH}$ , mA, at $U_{c1}=(13,5 \dots 16,5)\text{V}$ ; $U_{c2}=(-16,5 \dots -13,5)\text{V}$ ; $U^1=(4 \dots U_{c1})\text{V}$ .	-	0,2
Supply Current at High Level Reference, $I_{CCH}$ , mA, at $U_{c1}=(13,5 \dots 16,5)\text{V}$ ; $U_{c2}=(-16,5 \dots -13,5)\text{V}$ ; $U^0=(4 \dots U_{c1})\text{V}$ .		
Positive Power Supply	-	200
Negative Power Supply	-	5
Supply Current at Low Level Reference, $I_{cCL}$ , mA, at $U_{c1}=(13,5 \dots 16,5)\text{V}$ ; $U_{c2}=(-16,5 \dots -13,5)\text{V}$ ; $U^0=(0 \dots 0,8)\text{V}$ .		
Positive Power Supply	-	50
Negative Power Supply	-	5
Turn ON, $t_{on}$ , ns, at $U_{c1}=(13,5 \dots 16,5)\text{V}$ ; $U_{c2}=(-16,5 \dots -13,5)\text{V}$ ; $U^0=(0 \dots 0,8)\text{V}$ ; $U^{1ref}=(4\text{V} \dots U_{c1})$ ; $R_L=10\text{k}\Omega$ ; $C_L=40\text{nF}$		
(Pin 9, 16)	-	150
(Pin 4, 5)	-	300
Switch ON Resistance $R_{ON}$ , Om $U_{c1}=(13,5 \dots 16,5)\text{V}$ ; $U_{c2}=(-16,5 \dots -13,5)\text{V}$ ; $U^0=(0 \dots 0,8)\text{V}$ ; $U^{1ref}=(4 \dots U_{c1})\text{V}$ ; $U_S=(-15 \dots 15)\text{V}$ ; $I_S=1\text{mA}$	-	75