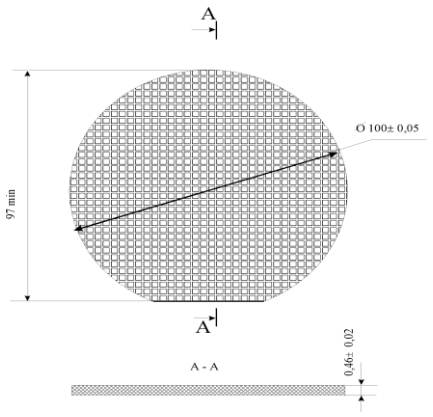
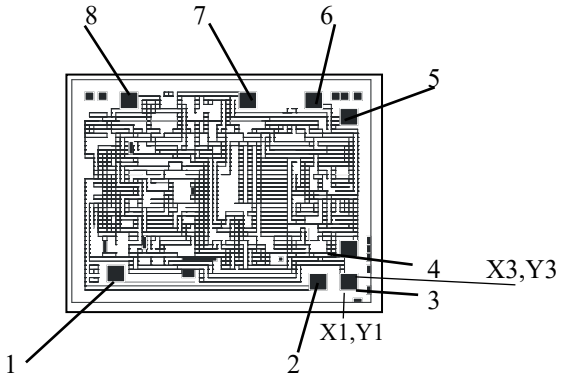
**αRD516H4 die on the common non-scribed wafer**

**Datasheet**

Scope: **High Voltage Operational Amplifier**




| | | Coordinates (mm) | | | |
|---|---|---|---|---|---|
| Pin No. | Description | X1 | Y1 | X3 | Y3 |
| 1 | Reference Input | 0,277 | 0,166 | 0,407 | 0,296 |
| 2 | Input - | 1,910 | 0,100 | 2,040 | 0,230 |
| 3 | Input + | 2,170 | 0,100 | 2,300 | 0,230 |
| 4 | Negative Power Supply $U_{cc2}$ | 2,163 | 0,360 | 0,293 | 0,490 |
| 5 | Reference Input | 2,163 | 1,438 | 0,293 | 1,568 |
| 6 | Output | 1,880 | 1,570 | 2,010 | 1,700 |
| 7 | Positive Power Supply $U_{cc1}$ | 1,358 | 1,570 | 1,488 | 1,700 |
| 8 | Slew Rate Correction | 0,382 | 1,570 | 0,512 | 1,700 |

**Electrical characteristics at T = 25±5 ºC**

| Parameter, units, conditions | | Limits | |
|---|---|---|---|
| | | min | max |
| Input Offset Voltage, $U_{IO}$, mV; | $U_{cc1}$ = 40V; $U_{cc2}$ =-40V; $R_L$ ≥ 5kOm | -5 | 5 |
| High Level Output Voltage, V; | $U_{cc1}$ = 40V; $U_{cc2}$ =-40V; $R_L$ ≥ 5kOm | 34 | - |
| High Level Output Voltage, V; | $U_{cc1}$ = 27V; $U_{cc2}$ =-27V; $R_L$ ≥ 5kOm | 21 | - |
| Input Bias Current, $I_I$, nA; | $U_{cc1}$ = 40V; $U_{cc2}$ =-40V; $R_L$ ≥ 5kOm | - | 20 |
| Input Offset Current, $I_{IO}$, nA; | $U_{cc1}$ = 40V; $U_{cc2}$ =-40V; $R_L$ ≥ 5kOm | - | 3 |
| Power Supply Current, $I_{CC}$, mA; | $U_{cc1}$ = 40V; $U_{cc2}$ =-40V; $R_L$ ≥ 5kOm | - | 4 |
| Gain Range, Au ; | $U_{cc1}$ = 40V; $U_{cc2}$ =-40V; $R_L$ ≥ 10kOm | $100 \cdot 10^3$ | - |
| Common-Mode Rejection Ratio, $K_{CMR}$, dB; | $U_{cc1}$ = 40V; $U_{cc2}$ =-40V; $R_L$ ≥ 5kOm | 80 | - |

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